

#### ABSTRACT OF THE DISCLOSURE

Predetermined programs and data are written into a first flash memory. The first flash memory is divided into a plurality of blocks. Security information for the respective blocks of the first flash memory is written into a second flash memory. The security information indicates whether or not reading of the stored data to the outside from the respective blocks of the first flash memory is prohibited. When a read target address signal is output by a CPU, the corresponding read data is read from the first flash memory and supplied to the CPU and a tri-state buffer. As a result, the CPU is able to obtain desired input data. A security signal from the second flash memory is supplied to the control terminal of the tri-state buffer. Accordingly, if reading of data to the outside is prohibited, the read data is not transmitted to the outside.